



Att. Docket No.: JCLA6643-R

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Serial No.: 09/801,350
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
Application No. : 09/801,350
Filed : March 7, 2001
For : ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT COUPLED
ON I/O PAD
Applicant : Lai et al.
Examiner : NADAV, ORI
Art Unit : 2811

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PRELIMINARY AMENDMENT

Assistant Commissioner of Patents and Trademarks
Washington, DC 20231

Sir:

Prior to an examination on the merits, please amend the above-identified patent application and consider the following remarks.

In The Claims:

Please substitute the following clean text claim for the pending claim of the same number.

B1 *sub*
1. (Amended) An electrostatic discharge (ESD) protection circuit, suitable for use on the I/O pad, the ESD protection circuit comprising:
a silicon controlled rectifier (SCR) circuit, which comprises a first connection terminal, a second connection terminal, and a third connection terminal, wherein the first connection